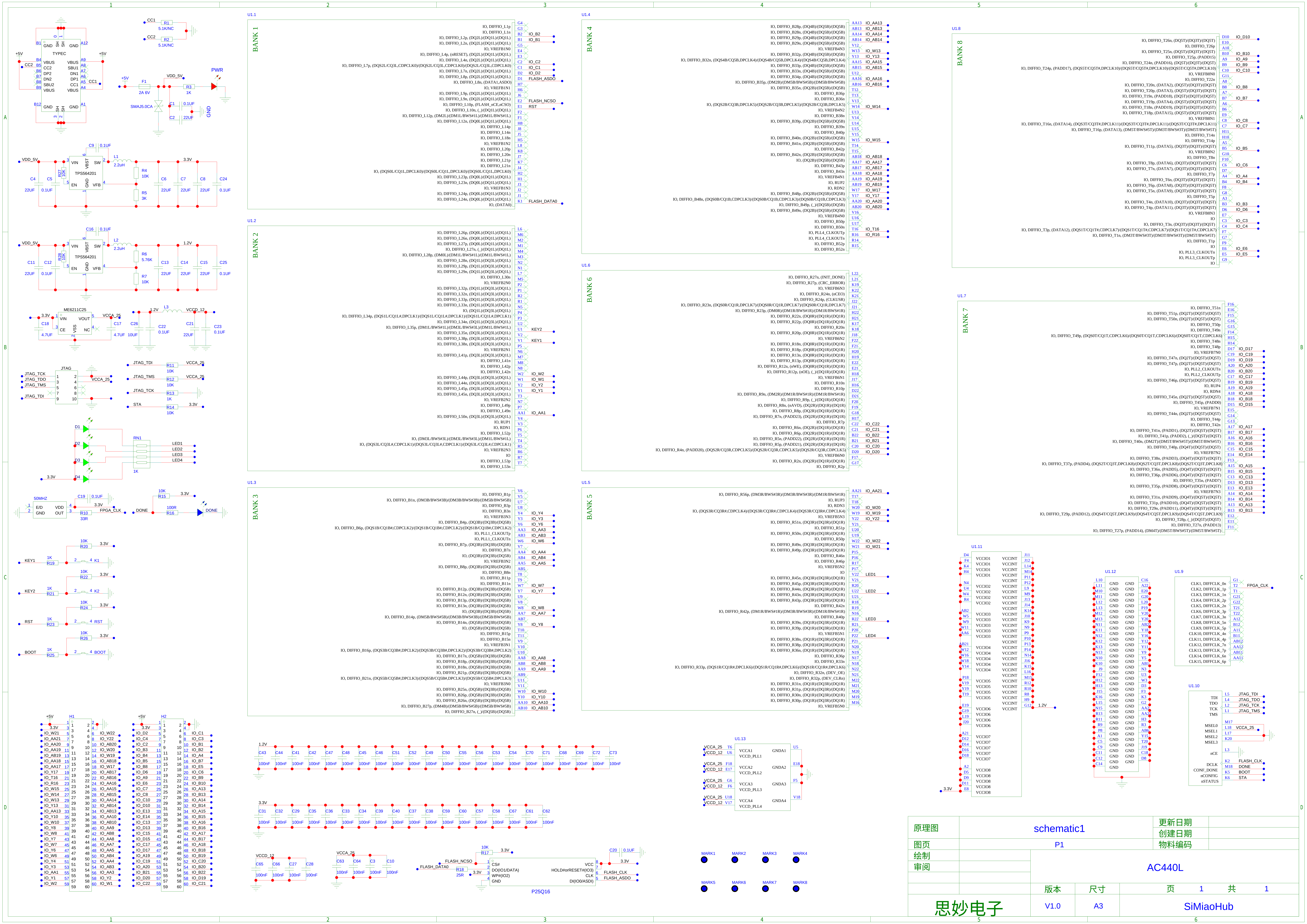
TYPEC
+5V
CC1
CC2
R1 5.1K/NC
R2 5.1K/NC
VDD_5V
PWR
F1 2A 6V
SMAJ5.0CA
R3 1K
C1 0.1UF
C2 22UF
GND
TPS564201
L1 2.2uH
3.3V
R4 10K
R5 3K
R27 10K
C9 0.1UF
C4 22UF
C5 0.1UF
C6 22UF
C7 22UF
C8 22UF
C24 0.1UF
C16 0.1UF
L2 2.2uH
1.2V
R6 5.76K
R7 10K
R28 10K
C11 22UF
C12 0.1UF
C13 22UF
C14 22UF
C15 22UF
C25 0.1UF
ME6211C25
VCCA_25
C18 4.7UF
C17 4.7UF
L3
VCCD_12
C26 10UF
C22 0.1UF
C21 22UF
C23 0.1UF
JTAG
JTAG_TCK
JTAG_TDO
JTAG_TMS
JTAG_TDI
R11 10K
R12 10K
R13 1K
R14 10K
STA
D1
D2
D3
D4
RN1 1K
LED1
LED2
LED3
LED4
50MHZ
C19 0.1UF
R10 33R
FPGA_CLK
DONE
R15 10K
R16 100R
KEY1
KEY2
RST
BOOT
R19 1K
R20 10K
R21 1K
R22 10K
R23 1K
R24 10K
R25 1K
R26 10K
K1
K2
H1
H2
U1.1 BANK 1
U1.2 BANK 2
U1.3 BANK 3
U1.4 BANK 4
U1.5 BANK 5
U1.6 BANK 6
U1.7 BANK 7
U1.8 BANK 8
U1.9
U1.10
U1.11
U1.12
U1.13
FLASH_NCSO
FLASH_DATA0
FLASH_CLK
FLASH_ASDO
R17 10K
R18 25R
C20 0.1UF
P25Q16
MARK1
MARK2
MARK3
MARK4
MARK5
MARK6
MARK7
MARK8
原理图
schematic1
更新日期
创建日期
图页
P1
物料编码
绘制
审阅
AC440L
版本
V1.0
尺寸
A3
页 1
共 1
思妙电子
SiMiaoHub