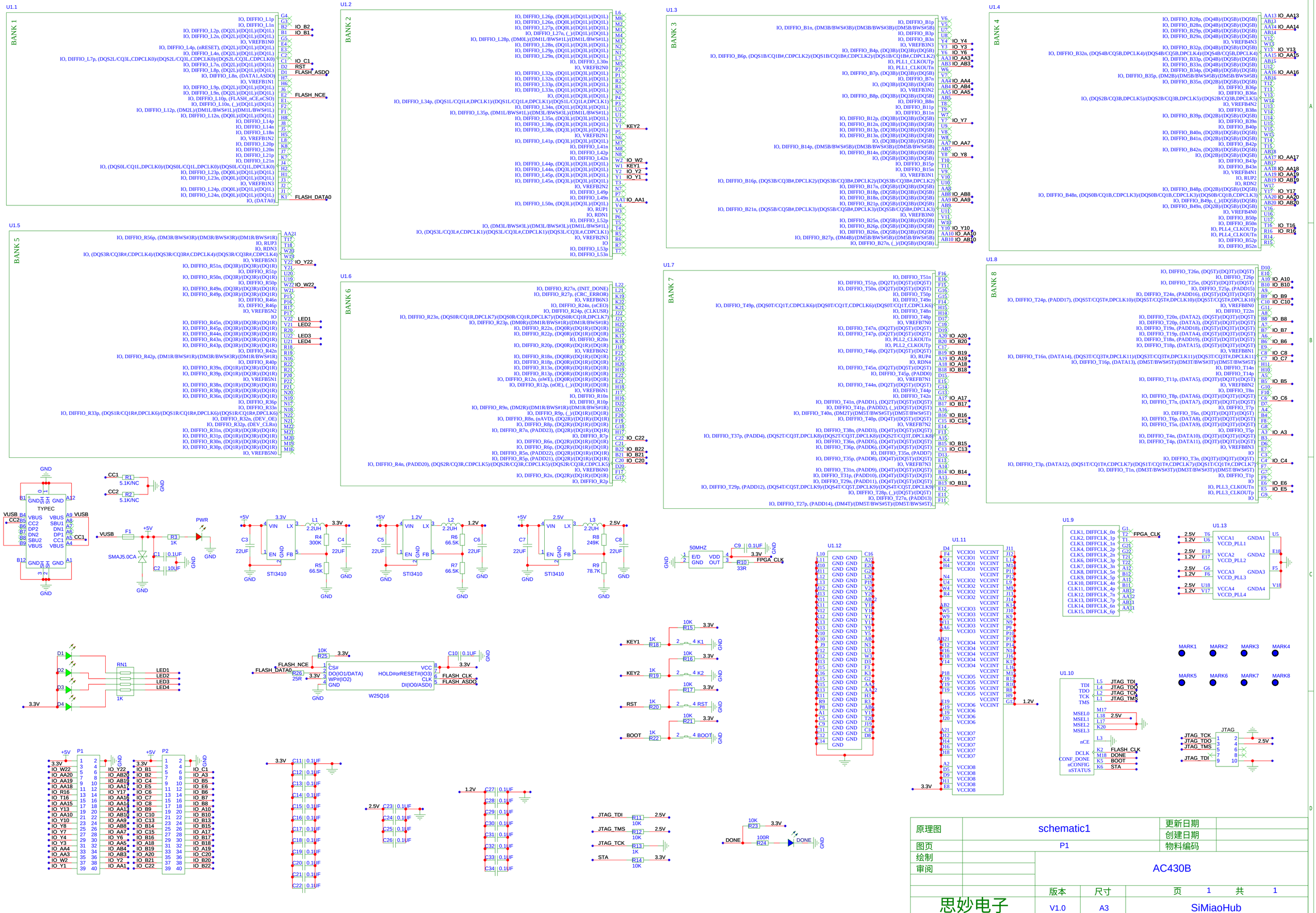
U1.1
BANK 1
IO, DIFFIO_L1p
IO, DIFFIO_L1n
IO, DIFFIO_L2p, (DQ2L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L2n, (DQ2L)/(DQ1L)/(DQ1L)
IO, VREFB1N0
IO, DIFFIO_L4p, (nRESET), (DQ2L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L4n, (DQ2L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L7p, (DQS2L/CQ3L,CDPCLK0)/(DQS2L/CQ3L,CDPCLK0)
IO, DIFFIO_L7n, (DQ2L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L8p, (DQ2L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L8n, (DATA1,ASDO)
IO, VREFB1N1
IO, DIFFIO_L9p, (DQ2L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L9n, (DQ2L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L10p, (FLASH_nCE,nCSO)
IO, DIFFIO_L10n, (_)/(DQ1L)/(DQ1L)
IO, DIFFIO_L12p, (DM2L)/(DM1L/BWS#1L)/(DM1L/BWS#1L)
IO, DIFFIO_L12n, (DQ0L)/(DQ0L)/(DQ1L)
IO, DIFFIO_L14p
IO, DIFFIO_L14n
IO, DIFFIO_L18n
IO, VREFB1N2
IO, DIFFIO_L20p
IO, DIFFIO_L20n
IO, DIFFIO_L21p
IO, DIFFIO_L21n
IO, (DQS0L/CQ1L,DPCLK0)/(DQS0L/CQ1L,DPCLK0)/(DQS0L/CQ1L,DPCLK0)
IO, DIFFIO_L23p, (DQ0L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L23n, (DQ0L)/(DQ1L)/(DQ1L)
IO, VREFB1N3
IO, DIFFIO_L24p, (DQ0L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L24n, (DQ0L)/(DQ1L)/(DQ1L)
IO, (DATA0),
IO_B2
IO_B1
IO_C1
RST
FLASH_ASDO
FLASH_NCE
FLASH_DATA0
U1.2
BANK 2
IO, DIFFIO_L26p, (DQ0L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L26n, (DQ0L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L27p, (DQ0L)/(DQ1L)/(DQ1L)
IO, DIFFIO_L27n, (_)/(DQ1L)/(DQ1L)
IO, DIFFIO_L28p, (DM0L)/(DM1L/BWS#1L)/(DM1L/BWS#1L)
IO, DIFFIO_L28n, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L29p, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L29n, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L30n
IO, VREFB2N0
IO, DIFFIO_L32p, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L32n, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L33p, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L33n, (DQ1L)/(DQ3L)/(DQ1L)
IO, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L34p, (DQS1L/CQ1L#,DPCLK1)/(DQS1L/CQ1L#,DPCLK1)/(DQS1L/CQ1L#,DPCLK1)
IO, DIFFIO_L34n, (DQ1L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L35p, (DM1L/BWS#1L)/(DM3L/BWS#3L)/(DM1L/BWS#1L)
IO, DIFFIO_L35n, (DQ3L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L38p, (DQ3L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L38n, (DQ3L)/(DQ3L)/(DQ1L)
IO, VREFB2N1
IO, DIFFIO_L41p, (DQ3L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L41n
IO, DIFFIO_L42p
IO, DIFFIO_L42n
IO, DIFFIO_L44p, (DQ3L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L44n, (DQ3L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L45p, (DQ3L)/(DQ3L)/(DQ1L)
IO, DIFFIO_L45n, (DQ3L)/(DQ3L)/(DQ1L)
IO, VREFB2N2
IO, DIFFIO_L49p
IO, DIFFIO_L49n
IO, DIFFIO_L50n, (DQ3L)/(DQ3L)/(DQ1L)
IO, RUP1
IO, RDN1
IO, DIFFIO_L52p
IO, (DM3L/BWS#3L)/(DM3L/BWS#3L)/(DM1L/BWS#1L)
IO, (DQS3L/CQ3L#,CDPCLK1)/(DQS3L/CQ3L#,CDPCLK1)/(DQS3L/CQ3L#,CDPCLK1)
IO, VREFB2N3
IO
IO, DIFFIO_L53p
IO, DIFFIO_L53n
KEY2
IO_W2
KEY1
IO_Y2
IO_Y1
U1.3
BANK 3
IO, DIFFIO_B1p
IO, DIFFIO_B1n, (DM3B/BWS#3B)/(DM3B/BWS#3B)/(DM5B/BWS#5B)
IO, DIFFIO_B3p
IO, DIFFIO_B3n
IO, VREFB3N3
IO, DIFFIO_B4p, (DQ3B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B6p, (DQS1B/CQ1B#,CDPCLK2)/(DQS1B/CQ1B#,CDPCLK2)/(DQS1B/CQ1B#,CDPCLK2)
IO, PLL1_CLKOUTp
IO, PLL1_CLKOUTn
IO, DIFFIO_B7p, (DQ3B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B7n
IO, (DQ3B)/(DQ3B)/(DQ5B)
IO, VREFB3N2
IO, DIFFIO_B8p, (DQ3B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B8n
IO, DIFFIO_B11p
IO, DIFFIO_B11n
IO, DIFFIO_B12p, (DQ3B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B12n, (DQ3B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B13p, (DQ3B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B13n, (DQ3B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, (DQ3B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B14p, (DM5B/BWS#5B)/(DM3B/BWS#3B)/(DM5B/BWS#5B)
IO, DIFFIO_B14n, (DQ5B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, (DQ5B)/(DQ3B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B15p
IO, DIFFIO_B15n
IO, VREFB3N1
IO, DIFFIO_B16p, (DQS3B/CQ3B#,DPCLK2)/(DQS3B/CQ3B#,DPCLK2)/(DQS3B/CQ3B#,DPCLK2)
IO, DIFFIO_B17n, (DQ5B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B18p, (DQ5B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B18n, (DQ5B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B21p, (DQ5B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B21n, (DQS5B/CQ5B#,DPCLK3)/(DQS5B/CQ5B#,DPCLK3)/(DQS5B/CQ5B#,DPCLK3)
IO, VREFB3N0
IO, DIFFIO_B25n, (DQ5B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B26p, (DQ5B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B26n, (DQ5B)/(DQ3B)/(DQ5B)
IO, DIFFIO_B27p, (DM4B)/(DM5B/BWS#5B)/(DM5B/BWS#5B)
IO, DIFFIO_B27n, (_)/(DQ5B)/(DQ5B)
IO_AA13
IO_AA14
IO_Y13
IO_AA15
IO_AA16
IO_AA4
IO_AB4
IO_AA5
IO_Y7
IO_AA7
IO_Y8
IO_W2
IO_Y1
IO_AB8
IO_AA9
IO_Y10
IO_AA10
IO_AB10
U1.4
BANK 4
IO, DIFFIO_B28p, (DQ4B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B28n, (DQ4B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B29p, (DQ4B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B29n, (DQ4B)/(DQ5B)/(DQ5B)
IO, VREFB4N3
IO, DIFFIO_B32p, (DQ4B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B32n, (DQS4B/CQ5B,DPCLK4)/(DQS4B/CQ5B,DPCLK4)/(DQS4B/CQ5B,DPCLK4)
IO, DIFFIO_B33p, (DQ4B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B33n, (DQ4B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B34p, (DQ4B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B35p, (DM2B)/(DM5B/BWS#5B)/(DM5B/BWS#5B)
IO, DIFFIO_B35n, (DQ2B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B36p
IO, DIFFIO_B36n
IO, (DQS2B/CQ3B,DPCLK5)/(DQS2B/CQ3B,DPCLK5)/(DQS2B/CQ3B,DPCLK5)
IO, VREFB4N2
IO, DIFFIO_B38n
IO, DIFFIO_B39p, (DQ2B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B39n
IO, DIFFIO_B40p
IO, DIFFIO_B40n, (DQ2B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B41n, (DQ2B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B42p
IO, DIFFIO_B42n, (DQ2B)/(DQ5B)/(DQ5B)
IO, (DQ2B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B43p
IO, DIFFIO_B43n
IO, VREFB4N1
IO, RUP2
IO, RDN2
IO, DIFFIO_B48p, (DQ2B)/(DQ5B)/(DQ5B)
IO, DIFFIO_B48n, (DQS0B/CQ1B,CDPCLK3)/(DQS0B/CQ1B,CDPCLK3)/(DQS0B/CQ1B,CDPCLK3)
IO, DIFFIO_B49p, (_)/(DQ5B)/(DQ5B)
IO, DIFFIO_B49n, (DQ2B)/(DQ5B)/(DQ5B)
IO, VREFB4N0
IO, DIFFIO_B50p
IO, DIFFIO_B50n
IO, PLL4_CLKOUTp
IO, PLL4_CLKOUTn
IO, DIFFIO_B52p
IO, DIFFIO_B52n
IO_AA13
IO_AA14
IO_Y13
IO_AA15
IO_AA16
IO_AA17
IO_AA18
IO_AA19
IO_AB19
IO_Y17
IO_AA20
IO_AB20
IO_T16
IO_R16
U1.5
BANK 5
IO, DIFFIO_R56p, (DM3R/BWS#3R)/(DM3R/BWS#3R)/(DM1R/BWS#1R)
IO, RUP3
IO, RDN3
IO, (DQS3R/CQ3R#,CDPCLK4)/(DQS3R/CQ3R#,CDPCLK4)/(DQS3R/CQ3R#,CDPCLK4)
IO, VREFB5N3
IO, DIFFIO_R51n, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R51p
IO, DIFFIO_R50n, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R50p
IO, DIFFIO_R49n, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R49p, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R46n
IO, DIFFIO_R46p
IO, VREFB5N2
IO
IO, DIFFIO_R45n, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R45p, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R44n, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R43n, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R43p, (DQ3R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R42n
IO, DIFFIO_R42p, (DM1R/BWS#1R)/(DM3R/BWS#3R)/(DM1R/BWS#1R)
IO, DIFFIO_R40p
IO, DIFFIO_R39n, (DQ1R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R39p, (DQ1R)/(DQ3R)/(DQ1R)
IO, VREFB5N1
IO, DIFFIO_R38n, (DQ1R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R38p, (DQ1R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R36n, (DQ1R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R36p
IO, DIFFIO_R33n
IO, DIFFIO_R33p, (DQS1R/CQ1R#,DPCLK6)/(DQS1R/CQ1R#,DPCLK6)/(DQS1R/CQ1R#,DPCLK6)
IO, DIFFIO_R32n, (DEV_OE)
IO, DIFFIO_R32p, (DEV_CLRn)
IO, DIFFIO_R31n, (DQ1R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R31p, (DQ1R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R30n, (DQ1R)/(DQ3R)/(DQ1R)
IO, DIFFIO_R30p, (DQ1R)/(DQ3R)/(DQ1R)
IO, VREFB5N0
IO_Y22
IO_W22
LED1
LED2
LED3
LED4
U1.6
BANK 6
IO, DIFFIO_R27n, (INIT_DONE)
IO, DIFFIO_R27p, (CRC_ERROR)
IO, VREFB6N3
IO, DIFFIO_R24n, (nCEO)
IO, DIFFIO_R24p, (CLKUSR)
IO, DIFFIO_R23n, (DQS0R/CQ1R,DPCLK7)/(DQS0R/CQ1R,DPCLK7)/(DQS0R/CQ1R,DPCLK7)
IO, DIFFIO_R23p, (DM0R)/(DM1R/BWS#1R)/(DM1R/BWS#1R)
IO, DIFFIO_R22n, (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R22p, (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R20n, (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R20p, (DQ0R)/(DQ1R)/(DQ1R)
IO, VREFB6N2
IO, DIFFIO_R18n, (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R18p, (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R13n, (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R13p, (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R12n, (nWE), (DQ0R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R12p, (nOE), (_)/(DQ1R)/(DQ1R)
IO, VREFB6N1
IO, DIFFIO_R10n
IO, DIFFIO_R10p
IO, DIFFIO_R9n, (DM2R)/(DM1R/BWS#1R)/(DM1R/BWS#1R)
IO, DIFFIO_R9p, (_)/(DQ1R)/(DQ1R)
IO, DIFFIO_R8n, (nAVD), (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R8p, (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R7n, (PADD23), (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R7p
IO, DIFFIO_R6n, (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R6p, (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R5n, (PADD22), (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R5p, (PADD21), (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R4n, (PADD20), (DQS2R/CQ3R,CDPCLK5)/(DQS2R/CQ3R,CDPCLK5)/(DQS2R/CQ3R,CDPCLK5)
IO, VREFB6N0
IO, DIFFIO_R2n, (DQ2R)/(DQ1R)/(DQ1R)
IO, DIFFIO_R2p
IO_C22
IO_B22
IO_B21
IO_C20
U1.7
BANK 7
IO, DIFFIO_T51n
IO, DIFFIO_T51p, (DQ2T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T50n, (DQ2T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T50p
IO, DIFFIO_T49n
IO, DIFFIO_T49p, (DQS0T/CQ1T,CDPCLK6)/(DQS0T/CQ1T,CDPCLK6)/(DQS0T/CQ1T,CDPCLK6)
IO, DIFFIO_T48n
IO, DIFFIO_T48p
IO, VREFB7N0
IO, DIFFIO_T47n, (DQ2T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T47p, (DQ2T)/(DQ5T)/(DQ5T)
IO, PLL2_CLKOUTn
IO, PLL2_CLKOUTp
IO, DIFFIO_T46p, (DQ2T)/(DQ5T)/(DQ5T)
IO, RUP4
IO, RDN4
IO, DIFFIO_T45n, (DQ2T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T45p, (PADD0)
IO, VREFB7N1
IO, DIFFIO_T44n, (DQ2T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T44p
IO, DIFFIO_T42n
IO, DIFFIO_T41n, (PADD1), (DQ2T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T41p, (PADD2), (_)/(DQ5T)/(DQ5T)
IO, DIFFIO_T40n, (DM2T)/(DM5T/BWS#5T)/(DM5T/BWS#5T)
IO, DIFFIO_T40p, (DQ4T)/(DQ5T)/(DQ5T)
IO, VREFB7N2
IO, DIFFIO_T38n, (PADD3), (DQ4T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T37p, (PADD4), (DQS2T/CQ3T,DPCLK8)/(DQS2T/CQ3T,DPCLK8)/(DQS2T/CQ3T,DPCLK8)
IO, DIFFIO_T36n, (PADD5), (DQ4T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T36p, (PADD6), (DQ4T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T35n, (PADD7)
IO, DIFFIO_T35p, (PADD8), (DQ4T)/(DQ5T)/(DQ5T)
IO, VREFB7N3
IO, DIFFIO_T31n, (PADD9), (DQ4T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T31p, (PADD10), (DQ4T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T29n, (PADD11), (DQ4T)/(DQ5T)/(DQ5T)
IO, DIFFIO_T29p, (PADD12), (DQS4T/CQ5T,DPCLK9)/(DQS4T/CQ5T,DPCLK9)/(DQS4T/CQ5T,DPCLK9)
IO, DIFFIO_T28p, (_)/(DQ5T)/(DQ5T)
IO, DIFFIO_T27n, (PADD13)
IO, DIFFIO_T27p, (PADD14), (DM4T)/(DM5T/BWS#5T)/(DM5T/BWS#5T)
IO_B14
IO_B13
U1.8
BANK 8
IO, DIFFIO_T26n, (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T26p
IO, DIFFIO_T25n, (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T25p, (PADD15)
IO, DIFFIO_T24n, (PADD16), (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T24p, (PADD17), (DQS5T/CQ5T#,DPCLK10)/(DQS5T/CQ5T#,DPCLK10)/(DQS5T/CQ5T#,DPCLK10)
IO, VREFB8N0
IO, DIFFIO_T22n
IO, DIFFIO_T20n, (DATA2), (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T20p, (DATA3), (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T19n, (PADD18), (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T19p, (DATA4), (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T18n, (PADD19), (DQ5T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T18p, (DATA15), (DQ5T)/(DQ3T)/(DQ5T)
IO, VREFB8N1
IO, DIFFIO_T16n, (DATA14), (DQS3T/CQ3T#,DPCLK11)/(DQS3T/CQ3T#,DPCLK11)/(DQS3T/CQ3T#,DPCLK11)
IO, DIFFIO_T16p, (DATA13), (DM5T/BWS#5T)/(DM3T/BWS#3T)/(DM5T/BWS#5T)
IO, DIFFIO_T14n
IO, DIFFIO_T14p
IO, DIFFIO_T11p, (DATA5), (DQ3T)/(DQ3T)/(DQ5T)
IO, VREFB8N2
IO, DIFFIO_T8n
IO, DIFFIO_T8p, (DATA6), (DQ3T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T7n, (DATA7), (DQ3T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T7p
IO, DIFFIO_T6n, (DQ3T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T6p, (DATA8), (DQ3T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T5n, (DATA9), (DQ3T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T5p
IO, DIFFIO_T4n, (DATA10), (DQ3T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T4p, (DATA11), (DQ3T)/(DQ3T)/(DQ5T)
IO, VREFB8N3
IO
IO, DIFFIO_T3n, (DQ3T)/(DQ3T)/(DQ5T)
IO, DIFFIO_T3p, (DATA12), (DQS1T/CQ1T#,DPCLK7)/(DQS1T/CQ1T#,DPCLK7)/(DQS1T/CQ1T#,DPCLK7)
IO, DIFFIO_T1n, (DM3T/BWS#3T)/(DM3T/BWS#3T)/(DM5T/BWS#5T)
IO, DIFFIO_T1p
IO
IO, PLL3_CLKOUTn
IO, PLL3_CLKOUTp
IO
IO_A10
IO_B10
IO_B9
IO_C10
IO_B8
IO_B7
IO_B6
IO_C8
IO_C7
IO_B5
IO_C6
IO_A3
IO_E6
IO_E5
U1.9
CLK1, DIFFCLK_0n
CLK2, DIFFCLK_1p
CLK3, DIFFCLK_1n
CLK4, DIFFCLK_2p
CLK5, DIFFCLK_2n
CLK6, DIFFCLK_3p
CLK7, DIFFCLK_3n
CLK8, DIFFCLK_5n
CLK9, DIFFCLK_5p
CLK10, DIFFCLK_4n
CLK11, DIFFCLK_4p
CLK12, DIFFCLK_7n
CLK13, DIFFCLK_7p
CLK14, DIFFCLK_6n
CLK15, DIFFCLK_6p
FPGA_CLK
U1.10
TDI
TDO
TCK
TMS
MSEL0
MSEL1
MSEL2
MSEL3
nCE
DCLK
CONF_DONE
nCONFIG
nSTATUS
JTAG_TDI
JTAG_TDO
JTAG_TCK
JTAG_TMS
2.5V
FLASH_CLK
DONE
BOOT
STA
U1.11
VCCIO1
VCCIO2
VCCIO3
VCCIO4
VCCIO5
VCCIO6
VCCIO7
VCCIO8
VCCINT
1.2V
U1.12
GND
U1.13
VCCA1
VCCD_PLL1
GNDA1
VCCA2
VCCD_PLL2
GNDA2
VCCA3
VCCD_PLL3
GNDA3
VCCA4
VCCD_PLL4
GNDA4
2.5V
1.2V
50MHZ
FPGA_CLK
3.3V
GND
MARK1
MARK2
MARK3
MARK4
MARK5
MARK6
MARK7
MARK8
JTAG
TYPEC
VUSB
CC1
CC2
5.1K/NC
SMAJ5.0CA
F1
PWR
1K
0.1UF
10UF
22UF
2.2UH
300K
66.5K
249K
78.7K
STI3410
W25Q16
FLASH_NCE
FLASH_DATA0
FLASH_CLK
FLASH_ASDO
LED1
LED2
LED3
LED4
RST
KEY1
KEY2
BOOT
100R
DONE
原理图
schematic1
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绘制
审阅
AC430B
更新日期
创建日期
物料编码
版本
V1.0
尺寸
A3
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